Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(substrate and trench and capacitor and silicon and transistor and (strap adj diffusion adj region) and divot). clm.	USPAT	OR	OFF	2005/09/27 08:57
L2	0	(substrate and trench and capacitor and silicon and transistor and (strap adj diffusion adj region) and divot). clm.	US-PGPUB	OR	OFF	2005/09/27 08:57